

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Icellyre Semiconductor, Inc., a Delaware Corporation, ("Assignor"), does hereby sell, assign, transfer and convey unto Zarbaña Digital Fund LLC, a Delaware limited liability company, having an office at 2711 Centerville Road, Suite 400, Wilmington, New Castle County, DE 19808 ("Assignee"), or its designees, all right, title and interest that exist today and may exist in the future in and to all of the following (the "Patent Rights"): (a) the provisional patent applications, patent applications and patents listed below; (b) all patents or patent applications to which any of the foregoing claim priority, and (c) current or future rights to (i) provisional patent applications, patent applications, and patents of any kind relating to any inventions and discoveries described in any provisional patent applications, patent applications and patents listed below; (ii) reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, and divisions of such patents and applications; and (iii) foreign counterparts to any of the foregoing, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants; (d) the rights to all inventions and discoveries described in any provisional patent application, patent application or patent listed below and all other rights arising out of such inventions and discoveries; (e) rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections or other governmental grants of any type related to the any of the foregoing categories (a), (b), (c) and (d), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding; (f) causes of action (whether currently pending, filed, or otherwise) and other enforcement rights, including, without limitation, all rights under the provisional patent applications, patent applications and patents listed below and/or under or on account of any of the foregoing categories (b), (c) and/or (d) to

- (i) damages,
- (ii) injunctive relief and
- (iii) other remedies of any kind

for past, current and future infringement; and

(g) all rights to collect royalties and other payments under or on account of any of the foregoing.

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-001	Switched-Mode Power Amplifier Integrally Performing Power Combining	Issued	6,603,352	U.S.A.	Wight, James	12/3/2001
ICE-001PC	Switched-Mode Power Amplifier Integrally Performing Power Combining	Nationalized	CA02/01847	PCT	Wight, James	12/3/2002
ICE-001JP	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2003-550250	Japan	Wight, James	12/3/2002
ICE-001KR	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	7008505/2004	Korea	Wight, James	06/03/2004
ICE-001CN	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2824126.6	China	Wight, James	12/3/2001
ICE-001AU**	Switched-Mode Power Amplifier Integrally Performing Power Combining	Lapsed	2002351903	Australia	Wight, James	12/3/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-001CP	Switched-Mode Power Amplifier Integrally Performing Power Combining (CIP)	Issued	6,937,096	U.S.A.	Wight, James	6/30/2003
ICE-002PR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Expired	60/307/889	U.S.A.	Wight, James	7/27/01
ICE-002	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	10/068,120	U.S.A.	Wight, James	2/6/2002
ICE-002PC	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Nationalized	CA02/01150	PCT	Wight, James	7/26/2002
ICE-002CA	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Abandoned	2455111	Canada	Wight, James	7/26/2002
ICE-002CN	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Pending	2818192.1	China	Wight, James	7/26/2002
ICE-002EP	Reception Diversity Combiner	Pending	2748525.9	EPO	Wight, James	7/26/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
	with Selectable Inversion and Variable Gain					
ICE-002JP	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	2003-518082	Japan	Wight, James	7/26/2002
ICE-002KR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Pending	7001206/2004	Korea	Wight, James	01/27/2004
ICE-002NO	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned but revivable	20040269	Norway	Wight, James	7/26/2002
ICE-003	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Allowed	10/094,826	U.S.A.	Wight, James	3/11/2002
ICE-003PC	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Expired	CA02/01498	PCT	Wight, James	10/4/2002
ICE-003AU**	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Lapsed	2002328744	Australia	Wight, James	10/4/2002
ICE-004	Up/Down Conversion Circuitry for Radio Transceiver	Pending	10/154,282	U.S.A.	Birkett, Alexander	5/22/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-004PC	Up/Down Conversion Circuitry for Radio Transceiver	Expired	CA02/01497	PCT	Birkett, Alexander	10/4/2002
ICE-004AU**	Up/Down Conversion Circuitry for Radio Transceiver	Lapsed	2002328743	Australia	Birkett, Alexander	10/4/2002
ICE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	U.S.A.	Birkett, Alexander	5/23/2002
ICE-005PC	Frequency Offset Generator for Synthesized Signals	Expired	CA02/01499	PCT	Birkett, Alexander	10/4/02
ICE-005AU**	Frequency Offset Generator for Synthesized Signals	Lapsed	2002328745	Australia	Birkett, Alexander	10/4/2002
ICE-006	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	10/273,908	U.S.A.	Parker, Kevin	10/18/2002
ICE-006JP	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	2004-543858	Japan	Parker, Kevin	04/15/2005

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-006AU**	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Lapsed	2003278003	Australia	Parker, Kevin	10/14/2003
ICE-006PC	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Expired	2004036862	PCT	Parker, Kevin	4/29/2004
ICE-007	Systems and Modules for Use with Trellis-Based Decoding	Pending	10/377,859	U.S.A.	Amer, Maher	2/28/2003
ICE-007PC	Viterbi Decoder Operating In Units Of a Plurality Of Transitions	Expired	CA04/000282	PCT	Amer, Maher	2/26/04
ICE-008PR	Parallel Convolutional Encoder	Expired	60/399,728	U.S.A.	Amer, Maher	8/1/2002
ICE-008	Parallel Convolutional Encoder	Pending	10/629,644	U.S.A.	Amer, Maher	7/29/2003
ICE-008KR	Parallel Convolutional Encoder	Pending	7001719/2005	Korea	Amer, Maher	01/31/2005
ICE-008CN	Parallel Convolutional Encoder	Pending	03818236.X	China	Amer, Maher	07/31/2003
ICE-008JP	Parallel Convolutional Encoder	Pending	2004-525088	Japan	Amer, Maher	03/24/2005

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-008PC	Parallel Convolutional Encoder	Nationalized	CA03/0113	PCT	Amer, Maher	07/31/03
ICE-008AU**	Parallel Convolutional Encoder	Lapsed	2003249822	Australia	Amer, Maher	7/31/2003
ICE-009PR	Parallel Scrambler Descrambler	Expired	60/411,343	U.S.A.	Amer, Maher	9/18/02
ICE-009	Parallel Scrambler/Descrambler	Pending	10/629,640	U.S.A.	Amer, Maher	7/29/2003
ICE-009PC	Parallel Scrambler/Descrambler	Expired	CA03/01132	PCT	Amer, Maher	7/31/2003
ICE-009AU**	Parallel Scrambler/Descrambler	Lapsed	2003249821	Australia	Amer, Maher	7/31/2003
ICE-010PR	Processing Engines and RF Circuitry for Multi-Carrier Modulation Transceivers	Expired	60/277,941	U.S.A.	Wright, James	3/23/01

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-010	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	09/918,106	U.S.A.	Wight, James	7/30/2001
ICE-010PC	Signal Decomposition for The Control Of its Dynamic Range	Nationalized	CA02/001174	PCT	Wight, James	7/29/2002
ICE-010CA	Signal Decomposition for The Control Of its Dynamic Range	Abandoned but Revivable	2,455,277	Canada	Wight, James	7/29/2002
ICE-010CN	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Pending	20818664.8	China	Wight, James	7/29/2002
ICE-010EP	Signal Decomposition for The Control Of its Dynamic Range	Pending	2748528.3	EPO	Wight, James	7/29/2002
ICE-010JP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	2003-518144	Japan	Wight, James	7/29/2002
ICE-010KR	Computational Circuits and Methods for Processing Modulated Signals Having	Pending	7001445/2004	Korea	Wight, James	01/30/2004

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
	Non-Constant Envelopes					
ICE-010NO	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned but Revivable	20040367	Norway	Wight, James	1/27/2004
ICE-010CP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Pending	10/205,743	U.S.A.	Wight, James	7/26/2002
ICE-011	Chireix Architecture Using Low Impedance Amplifiers	Issued	6836183	U.S.A.	Wight, James	10/16/2002
ICE-011JP	Chireix Architecture Using Low Impedance Amplifiers	Pending	2004-543859	Japan	Wight, James	04/15/2005
ICE-011PC	Chireix Architecture Using Low Impedance Amplifiers	Nationalized	CA03/001546	PCT	Wight, James	10/14/2003
ICE-011EP	Chireix Architecture Using Low Impedance Amplifiers	Pending	03769084	EPO	Wight, James	10/14/2003

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ BLACK BORDERS
- ☐ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES
- ☐ FADED TEXT OR DRAWING
- ☐ BLURRED OR ILLEGIBLE TEXT OR DRAWING
- ☐ SKEWED/SLANTED IMAGES
- ☐ COLOR OR BLACK AND WHITE PHOTOGRAPHS
- ☐ GRAY SCALE DOCUMENTS
- ☒ LINES OR MARKS ON ORIGINAL DOCUMENT
- ☐ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY
- ☐ OTHER: _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.